

| | Type | Ref # | Hits | Search Text |
|---|------|-------|-------|--|
| 1 | IS&R | S1 | 1975 | (315/169.3).CCLS. |
| 2 | BRS | S2 | 892 | S1 and @pd<"20030121" |
| 3 | BRS | S3 | 701 | (integrated adj circuit) and (different adj voltage\$1) and CMOS and PMOS and NMOS |
| 4 | BRS | S4 | 432 | S3 and @pd<"20030121" |
| 5 | BRS | S6 | 4 | (level near2 transfor\$4) and (control near1 signal) and buffer\$1 and (overvoltage adj protection) |
| 6 | BRS | S7 | 29819 | (integrated adj circuit\$1) and voltage same (low and high) same (adjust or transistion or level) |

| | DBs | Time Stamp | Comments |
|----------|---|-----------------------------|-----------------|
| 1 | US-PGPUB; USPAT; USOCR | 2005/04/25 11:23 | |
| 2 | USPAT | 2005/04/25 13:36 | |
| 3 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | 2005/04/25 13:37 | |
| 4 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | 2005/04/26 09:48 | |
| 5 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | 2005/04/26 09:52 | |
| 6 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | 2005/04/26 09:54 | |

| | Error Definition | Errors |
|----------|-----------------------------|---------------|
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| | Type | Ref # | Hits | Search Text |
|----|------|-------|-------|---|
| 7 | BRS | S8 | 22918 | S7 and @pd<"20030121" |
| 8 | BRS | S9 | 8477 | S8 and (control\$4 adj circuit\$1) |
| 9 | BRS | S10 | 24 | S9 and (level adj transfor\$4) |
| 10 | BRS | S11 | 4055 | S9 and buffer\$1 |
| 11 | BRS | S12 | 39 | S11 and (overvoltage near1 protection) |

| | DBs | Time Stamp | Comments |
|----|---|-----------------------------|-----------------|
| 7 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | 2005/04/26 09:54 | |
| 8 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | 2005/04/26 10:15 | |
| 9 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | 2005/04/26 09:56 | |
| 10 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | 2005/04/26 10:15 | |
| 11 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | 2005/04/26 10:16 | |

| | Error Definition | Errors |
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| | Type | Ref # | Hits | Search Text |
|----|------|-------|------|---|
| 12 | BRS | S13 | 14 | S12 and semiconductor |
| 13 | BRS | S14 | 1718 | integrated same (high and low) same voltage same MOS |
| 14 | BRS | S16 | 998 | S14 and @pd<"20030121" |
| 15 | BRS | S17 | 297 | S16 and interface |
| 16 | BRS | S21 | 2459 | (level with conversion with circuit) and voltage and semiconductor |
| 17 | BRS | S22 | 1744 | S21 and @pd<"20030121" |

| | DBs | Time Stamp | Comments |
|-----------|---|-----------------------------|-----------------|
| 12 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | 2005/04/26 10:57 | |
| 13 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | 2005/04/26 10:58 | |
| 14 | USPAT | 2005/04/26 10:59 | |
| 15 | USPAT | 2005/04/26 11:21 | |
| 16 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | 2005/04/26 11:25 | |
| 17 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | 2005/04/26 11:26 | |

| | Error Definition | Errors |
|-----------|-----------------------------|---------------|
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| | Type | Ref # | Hits | Search Text |
|----|------|-------|------|---|
| 18 | BRS | S23 | 730 | S22 and MOS |
| 19 | BRS | S24 | 621 | S23 and control |
| 20 | BRS | S25 | 652 | S23 and control\$4 |
| 21 | BRS | S26 | 9 | S25 and overvoltage |
| 22 | BRS | S27 | 7382 | interface and voltage same (low and high) and MOS and integrated |

| | DBs | Time Stamp | Comments |
|-----------|---|-----------------------------|-----------------|
| 18 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | 2005/04/26 11:26 | |
| 19 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | 2005/04/26 11:27 | |
| 20 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | 2005/04/26 11:27 | |
| 21 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | 2005/04/26 12:44 | |
| 22 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | 2005/04/26 12:45 | |

| | Error Definition | Errors |
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| | Type | Ref # | Hits | Search Text |
|----|------|-------|------|--|
| 23 | BRS | S28 | 5056 | S27 and @pd<"20030121" |
| 24 | BRS | S29 | 4630 | S28 and control\$4 |
| 25 | BRS | S30 | 2383 | S29 and buffer\$1 |
| 26 | BRS | S31 | 108 | S30 and overvoltage |
| 27 | BRS | S32 | 349 | (level adj conversion adj circuit) and (semiconductor adj integrated adj circuit) |

| | DBs | Time Stamp | Comments |
|-----------|---|-----------------------------|-----------------|
| 23 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | 2005/04/26 12:45 | |
| 24 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | 2005/04/26 12:46 | |
| 25 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | 2005/04/26 12:48 | |
| 26 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | 2005/04/26 12:54 | |
| 27 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | 2005/04/26 12:55 | |

| | Error Definition | Errors |
|-----------|-----------------------------|---------------|
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| | Type | Ref # | Hits | Search Text |
|----|------|-------|-------|---|
| 28 | BRS | S33 | 259 | S32 and @pd<"20030121" |
| 29 | BRS | S34 | 20782 | semiconductor and (IC or (integrated adj circuit)) and inverter and transistor |
| 30 | BRS | S35 | 193 | S34 and (315/291 or 315/224 or 315/307 or 315/160) |
| 31 | IS&R | S36 | 1 | ("6473359").PN. |
| 32 | BRS | S37 | 4812 | S34 and ("315"/\$.ccIs. or "327"/\$.ccIs.) |
| 33 | BRS | S38 | 3789 | S37 and @pd<"20030121" |

| | DBs | Time Stamp | Comments |
|----|---|-----------------------------|-----------------|
| 28 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | 2005/04/26 14:36 | |
| 29 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | 2005/04/26 14:37 | |
| 30 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | 2005/04/26 14:52 | |
| 31 | US-PGPUB; USPAT; USOCR | 2005/04/26 14:57 | |
| 32 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | 2005/04/26 15:02 | |
| 33 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | 2005/04/26 15:02 | |

| | Error Definition | Errors |
|-----------|-----------------------------|---------------|
| 28 | | |
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| | Type | Ref # | Hits | Search Text |
|----|------|-------|------|---------------------------------------|
| 34 | BRS | S39 | 3607 | S38 and voltage |
| 35 | BRS | S40 | 103 | S39 and (level adj conversion) |
| 36 | BRS | S41 | 1040 | 326/80 |
| 37 | BRS | S42 | 759 | S41 and @pd<"20030121" |
| 38 | BRS | S43 | 1509 | 326/81 |

| | DBs | Time Stamp | Comments |
|----|---|-----------------------------|-----------------|
| 34 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | 2005/04/26 15:02 | |
| 35 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | 2005/04/26 15:22 | |
| 36 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | 2005/04/26 15:23 | |
| 37 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | 2005/04/29 13:55 | |
| 38 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | 2005/04/26 15:25 | |

| | Error Definition | Errors |
|-----------|-----------------------------|---------------|
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| | Type | Ref # | Hits | Search Text |
|----|------|-------|------|---|
| 39 | BRS | S44 | 1061 | S43 and @pd<"20030121" |
| 40 | BRS | S45 | 5 | "level transforming circuit" and "interface" |
| 41 | BRS | S46 | 15 | "level transforming circuit" |
| 42 | IS&R | S47 | 3012 | (365/230.06).CCLS. |
| 43 | BRS | S48 | 1334 | S47 and driver and (interfac\$3 or level\$3) |
| 44 | IS&R | S49 | 1 | ("6504400").PN. |

| | DBs | Time Stamp | Comments |
|----|---|-----------------------------|-----------------|
| 39 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | 2005/04/26 15:26 | |
| 40 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | 2005/04/29 13:57 | |
| 41 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | 2005/04/29 13:57 | |
| 42 | US-PGPUB; USPAT; USOCR | 2005/04/29 14:57 | |
| 43 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | 2005/04/29 15:01 | |
| 44 | US-PGPUB; USPAT; USOCR | 2005/04/29 15:01 | |

| | Error Definition | Errors |
|-----------|-----------------------------|---------------|
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